

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 250793US2S DIV		SERIAL NO. 10/809377 New Divisional Application	
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Keiichi KUSHIDA					
		FILING DATE Herewith		GROUP 2824			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
104	AA	2003/0147271 A1	08-2003	Jo, Seong-Kue	—	—	
104	AB	5,375,086 A	12-1994	Wahlstrom, Sven E.	—	—	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION		
					YES	NO	
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
104	AW	Hiep TRAN, "Demonstration of 5T SRAM and 6T Dual-Port RAM Cell Arrays", 1996 SYMPOSIUM ON VLSI CIRCUITS DIGEST OF TECHNICAL PAPERS, 1996, pgs. 68-69.					
	AX						
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner Vu Le		Date Considered 08/17/04					
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							